



DOCKET NO.: 4363P015

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

ERIC SOENEN, ET AL.

Application No.: 10/656,793

Filed: September 05, 2003

For: **Improved Capacitor Structure And
Automated Design Flow For Incorporating
Same**

Art Group: 2826

Examiner: Kevin V. Quinto

Date of mailing of PTOL 85 entitled
"Notice of Allowance and Fee(s) Due" May
05, 2005

Confirmation No. 7097

Mail Stop ISSUE FEE

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

TRANSMITTAL OF FORMAL DRAWINGS

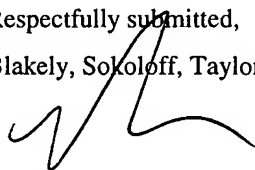
Sir:

Enclosed herewith for filing in the above-identified U.S. Patent Application are the formal drawings, 8 sheets including 12 Figures. The three month period of response set in the Notice of Allowability (PTOL 37) expires on August 5, 2005. This submission is on or before this expiration date. Please charge any additional extension or petition fees under 37 C.F.R. §1.17 or credit for any overpayment to our Deposit Account No. 02-2666. A duplicate copy of the Fee Transmittal sheet is enclosed.

Respectfully submitted,

Blakely, Sokoloff, Taylor & Zafman LLP

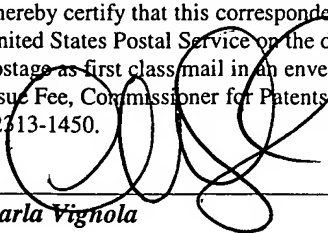
Dated: 8/5/05


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CERTIFICATE OF MAILING/TRANSMISSION

I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Mail Stop Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.


Carla Vignola

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